

<b>Notice of References Cited</b>	Application/Control No. 10/043,223	Applicant(s)/Patent Under Reexamination VINITZKY, GIL	
	Examiner Brett J Buehl	Art Unit 2183	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,933,627	08-1999	Parady, Bodo	712/228
	B	US-2003/0046517	03-2003	Lauterbach, Gary R.	712/214
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Goossens, Bernard; Duc Thang Vu. Multithreading to improve cycle width and CPI in superpipelined superscalar procesors. Proceedings. Second International Symposium on Parallel Architectures, Algorithms and Networks. 12-14 June 1996. Pages: 36-42.
	V	Hennessy, John; Patterson, David. Computer Organization and Design: the hardware/software interface. Morgan Kaufmann Publishers. 2nd Edition. Page 510.
	W	Culler, David E., Jaswinder Pal Singh, with Anoop Gupta. Parallel Computer Architecture: A hardware/software approach. August 1998. Morgan Kaufmann. 1st Edition. Chapter 11, Pages 902-911.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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